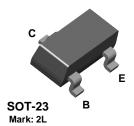


# 2N5401

# **MMBT5401**





# **PNP General Purpose Amplifier**

This device is designed as a general purpose amplifier and switch for applications requiring high voltages.

## **Absolute Maximum Ratings\***

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
$V_{CEO}$	Collector-Emitter Voltage	150	V	
V <sub>CBO</sub>	Collector-Base Voltage	160	V	
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V	
I <sub>C</sub>	Collector Current - Continuous	600	mA	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range -55 to +150 °C		°C	

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- These ratings are based on a maximum junction temperature of 150 degrees C.
   These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
   All voltages (V) and currents (A) are negative polarity for PNP transistors.

## **Thermal Characteristics**

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		2N5401	*MMBT5401	
P <sub>D</sub>	Total Device Dissipation	625	350	mW
	Derate above 25°C	5.0	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

# **PNP General Purpose Amplifier**

(continued)

Electrical Characteris	

TA = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
	RACTERISTICS				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 1.0 \text{ mA}, I_{\rm B} = 0$	150		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 100  \mu A, I_E = 0$	160		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = 10  \mu A,  I_C = 0$	5.0		V
I <sub>CBO</sub>	Collector Cutoff Current	$V_{CB} = 120 \text{ V}, I_E = 0$ $V_{CB} = 120 \text{ V}, I_E = 0, T_A = 100^{\circ}\text{C}$		50 50	nA μA
I <sub>EBO</sub>	Emitter Cutoff Current	$V_{EB} = 3.0 \text{ V}, I_{C} = 0$		50	nΑ
		$I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ V}$ $I_C = 50 \text{ mA}, V_{CE} = 5.0 \text{ V}$	60 50	240	
h <sub>FF</sub>	ACTERISTICS*  DC Current Gain	$I_C = 1.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$	50		
		$I_C = 50 \text{ mA}, V_{CE} = 5.0 \text{ V}$		2.10	
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1.0 \text{ mA}$		0.2	V
\ /	Base-Emitter Saturation Voltage	$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$ $I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$		0.5 1.0	V
$V_{BE(sat)}$	Base-Emilier Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 1.0 \text{ mA}$ $I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		1.0	V
SMALL S	IGNAL CHARACTERISTICS  Current Gain - Bandwidth Product	$I_{\rm C} = 10$ mA, $V_{\rm CF} = 10$ V,	100	300	MHz
	Current Gain - Bandwidth Product	f = 100  MHz	100	300	IVITZ
C <sub>obo</sub>	Output Capacitance	$V_{CB} = 10 \text{ V}, I_{E} = 0,$ f = 1.0 MHz		6.0	pF
NF	Noise Figure	$\begin{split} I_C &= 250 \; \mu\text{A}, \; V_{CE} = 5.0 \; \text{V}, \\ R_S &= 1.0 \; \text{k}\Omega, \\ f &= 10 \; \text{Hz to 15.7 kHz} \end{split}$		8.0	dB

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

**NOTE:** All voltages (V) and currents (A) are negative polarity for PNP transistors.

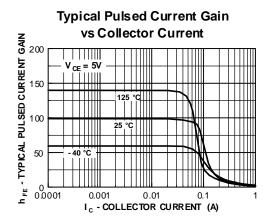
# **Spice Model**

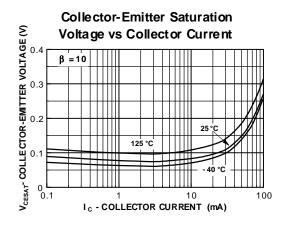
 $PNP \ (Is=21.48f \ Xti=3 \ Eg=1.11 \ Vaf=100 \ Bf=132.1 \ Ne=1.375 \ Is=21.48f \ Ikf=.1848 \ Xtb=1.5 \ Br=3.661 \ Nc=2 \ Isc=0 \ Ikr=0 \ Rc=1.6 \ Cjc=17.63p \ Mjc=.5312 \ Vjc=.75 \ Fc=.5 \ Cje=73.39p \ Mje=.3777 \ Vje=.75 \ Tr=1.476n \ Tf=641.9p \ Itf=0 \ Vtf=0 \ Xtf=0 \ Rb=10)$ 

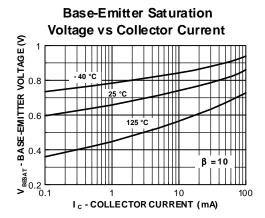
## **PNP General Purpose Amplifier**

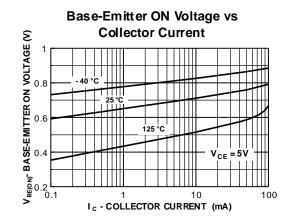
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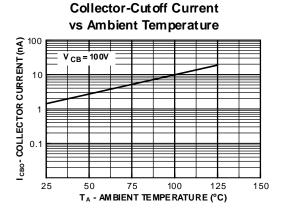
## **Typical Characteristics**

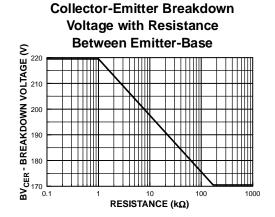








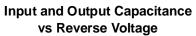


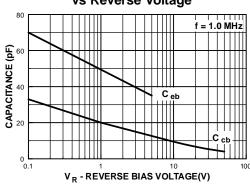


# **PNP General Purpose Amplifier**

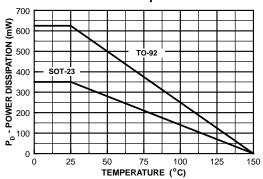
(continued)

## **Typical Characteristics** (continued)





## Power Dissipation vs Ambient Temperature



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